

Take advantage of three ways to save with these new 16900 Series logic analyzer promotions

Save 50% on 16900 Series memory modules

When you purchase a new 16900 Series logic analyzer acquisition module, you qualify to receive a 50% discount on 16M, 32M, or 64M memory options. Promotion lasts through August 31, 2006. Promotion code 5.504



Save 50% on a FPGA dynamic probe

Purchase either a 16900A, 16902A, or a 16903A logic analysis mainframe and we'll give you a 50% discount on our award-winning B4655A FPGA dynamic probe. Promotion lasts through August 31, 2006. Promotion Code 5.503

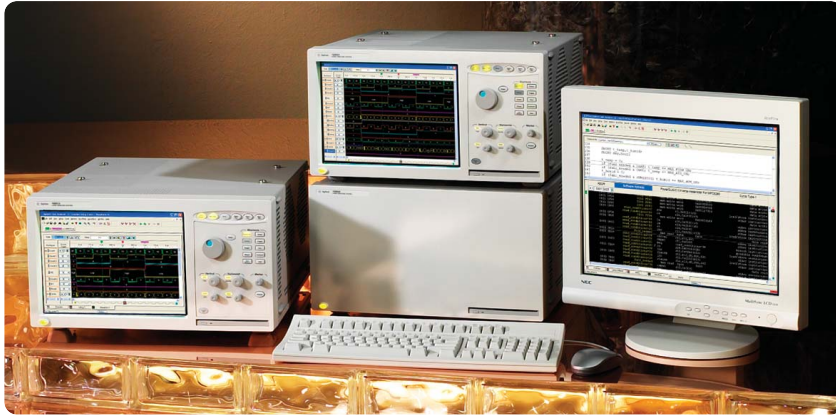
Get a free 20" flat-panel display with mainframe purchase

When you purchase a new 16902A or 16903A logic analysis mainframe, you will qualify to receive a 20" flat-panel display at no additional charge. Offer good through December 31, 2006. Promotion Code 5.520

www.agilent.com/find/LA_Promotions



Agilent Technologies



16900 Series logic analysis mainframes

- **Full-featured, intuitive operation** - Set up measurements easily and navigate through your data quickly with the analyzer's intuitive user interface and the familiarity of Windows® XP Professional.
- **Modular and expandable** - Purchase the capability you need now, then expand as your needs evolve. Supports 14 different logic analyzer modules.
- **Innovative probing** - Connect to your system under test with a wide variety of probing accessories.
- **Hosted performance mode** - Achieve the fastest logic analyzer control and analysis in the industry with a multi-threading architecture that takes full advantage of Gbit LAN.
- **Broad processor, FPGA, ASIC and bus support** - Debug at full-speed with non-intrusive, real-time analysis tools for the devices and buses in your digital system.

- **Wide range of use models** - Increase your productivity with offline analysis, remote control and programmability features.

Learn more at www.agilent.com/find/logic

16900 Series logic analysis modules

- Measure precise timing relationships with timing zoom
- Find anomalies separated in time with deep memory depths
- Sample high-speed synchronous buses accurately and confidently using eye scan
- Upgrade your memory depth and state speed as your needs change
- Track problems from symptom to root cause across several measurement modes by viewing time-correlated data in *waveform/chart*, *listing*, *inverse assembly*, *source code*, or *compare* display
- Capture either single-ended or differential signals

Learn more at www.agilent.com/find/logic

B4655A FPGA dynamic probe

- Gain visibility into your FPGA's internal activity. Access up to 64 internal signals for each pin dedicated to debug.
- Switch internal probe points in seconds without changing your FPGA design.
- Leverage the work you did in your design environment. The FPGA dynamic probe maps internal signal naming from your FPGA design tool to your logic analyzer. Automated bus name and signal setup eliminates mistakes and saves time.
- Supports Xilinx Virtex-4 series, Virtex-II Pro series, Virtex-II series, and Spartan-3 series devices.

Learn more at www.agilent.com/find/logic
www.agilent.com

Product specifications and descriptions in this document subject to change without notice.

Windows is a U.S. registered trademark of Microsoft Corporation.

© Agilent Technologies, Inc. 2006
Printed in USA, May 3, 2006
5989-5128EN



Agilent Technologies